

Figure 1

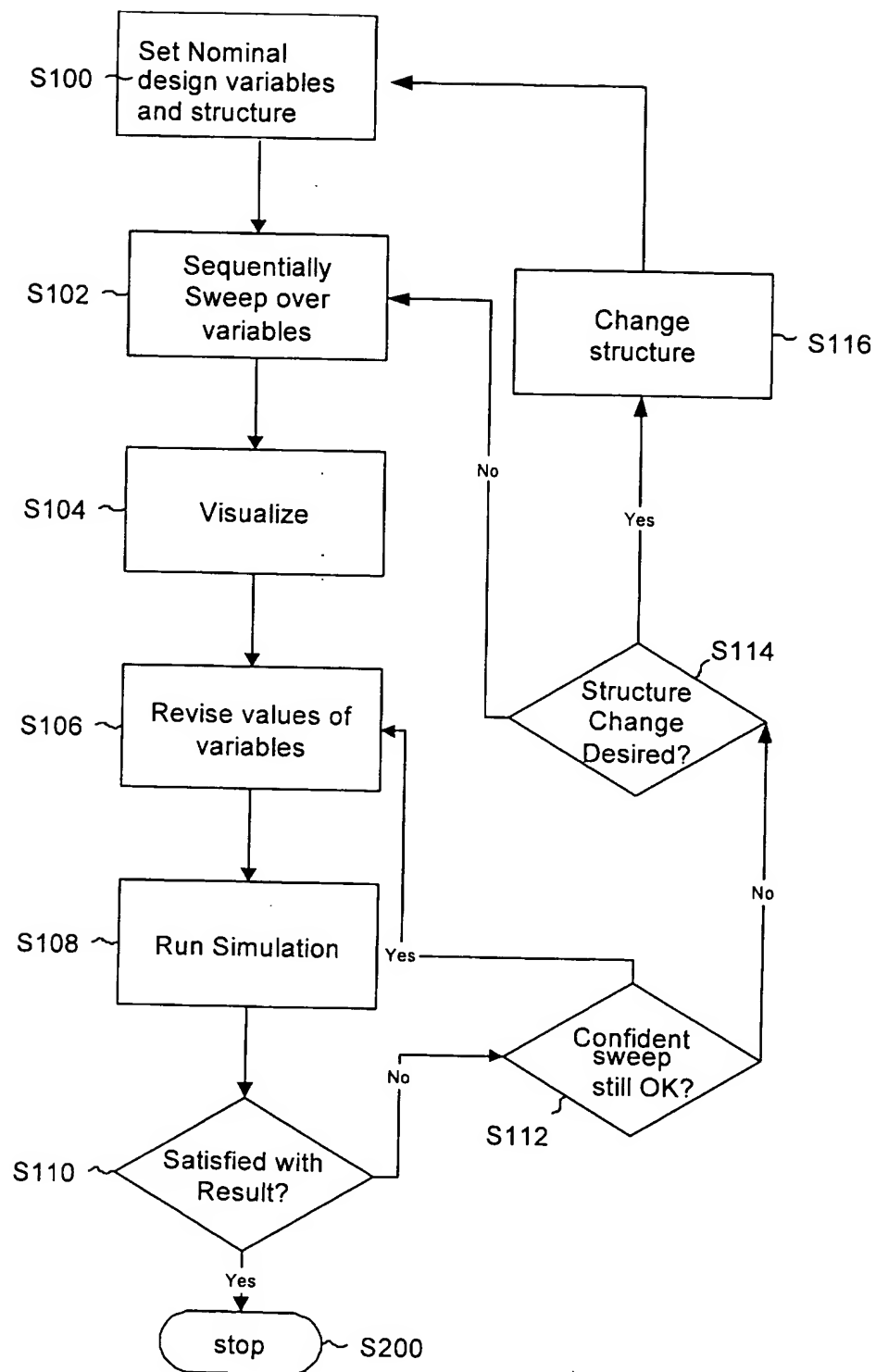


Figure 2

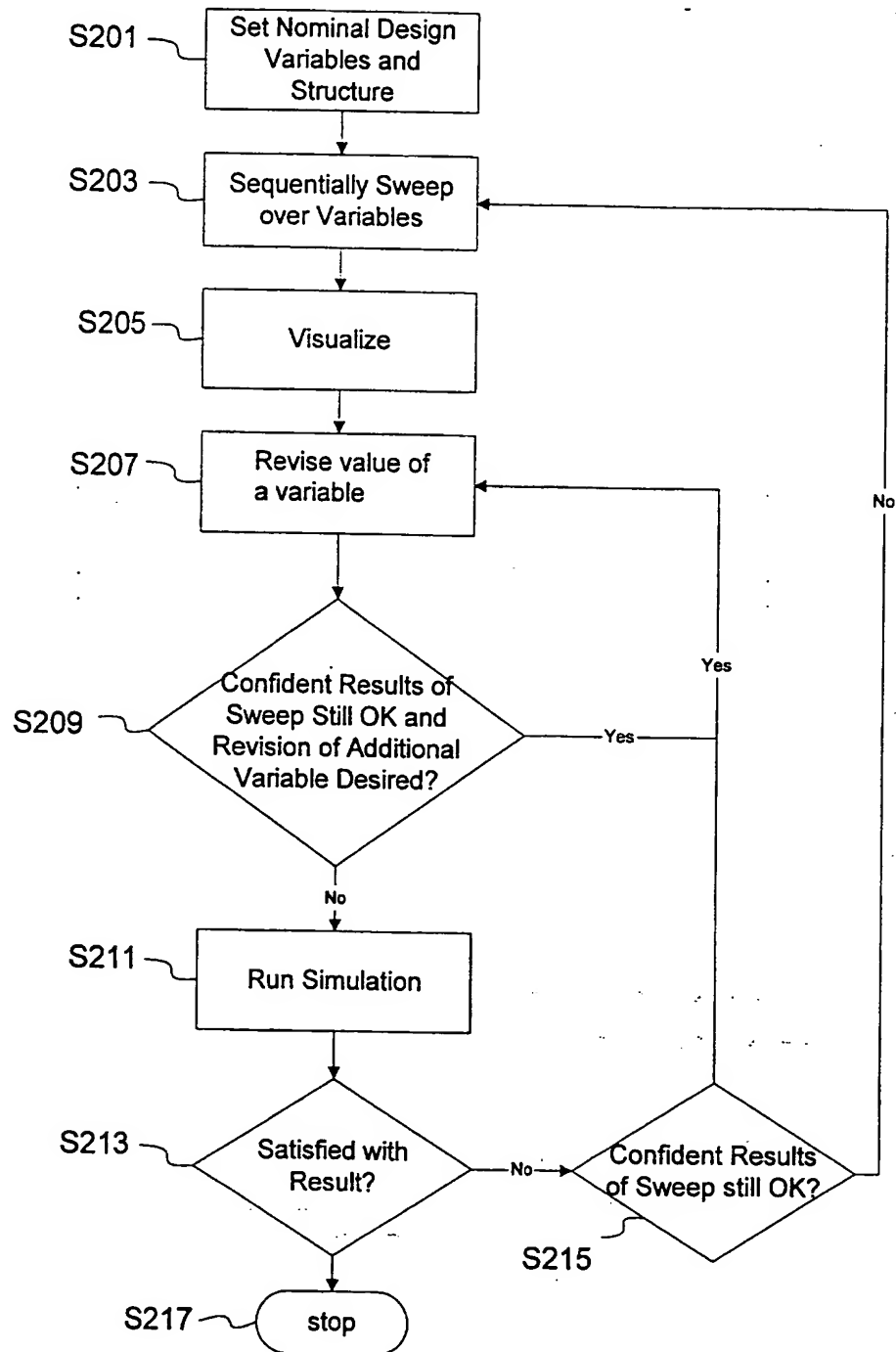


Figure 3

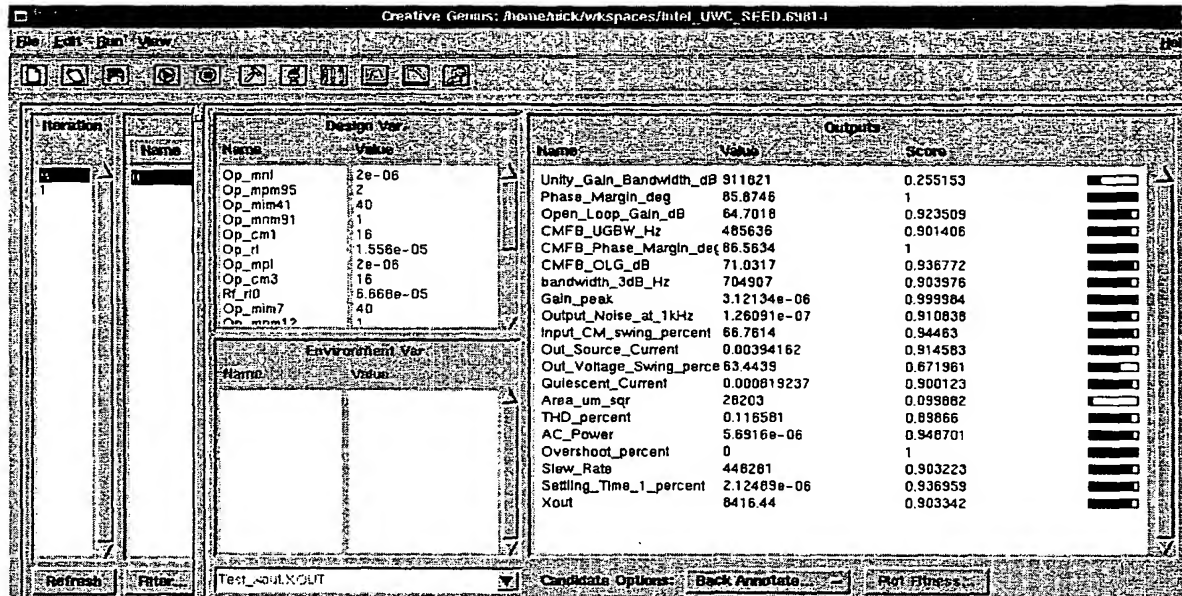


Figure 4

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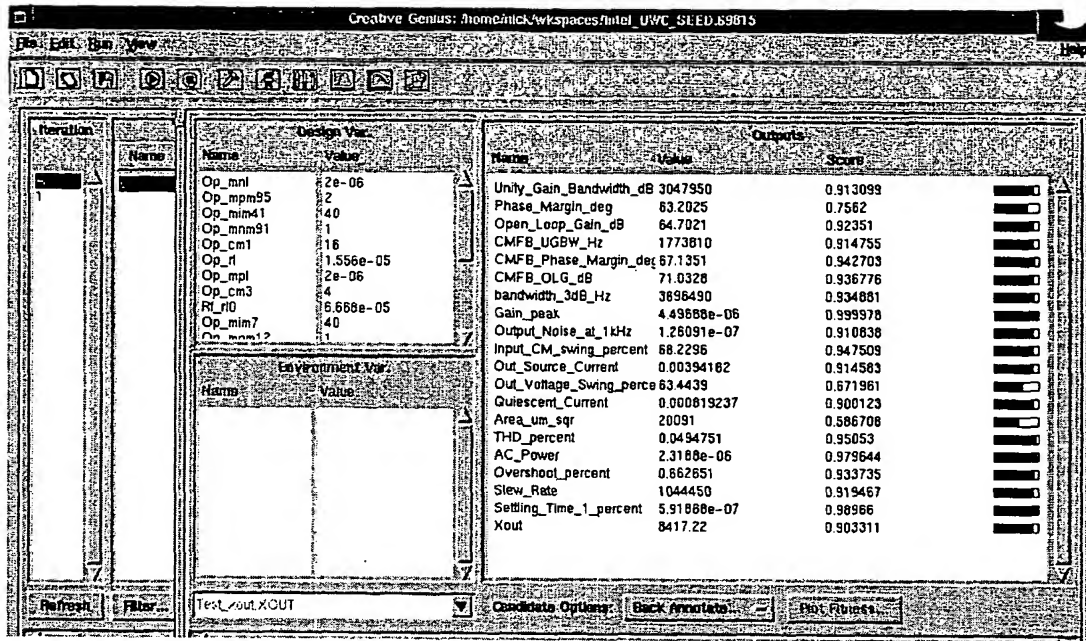


Figure 5

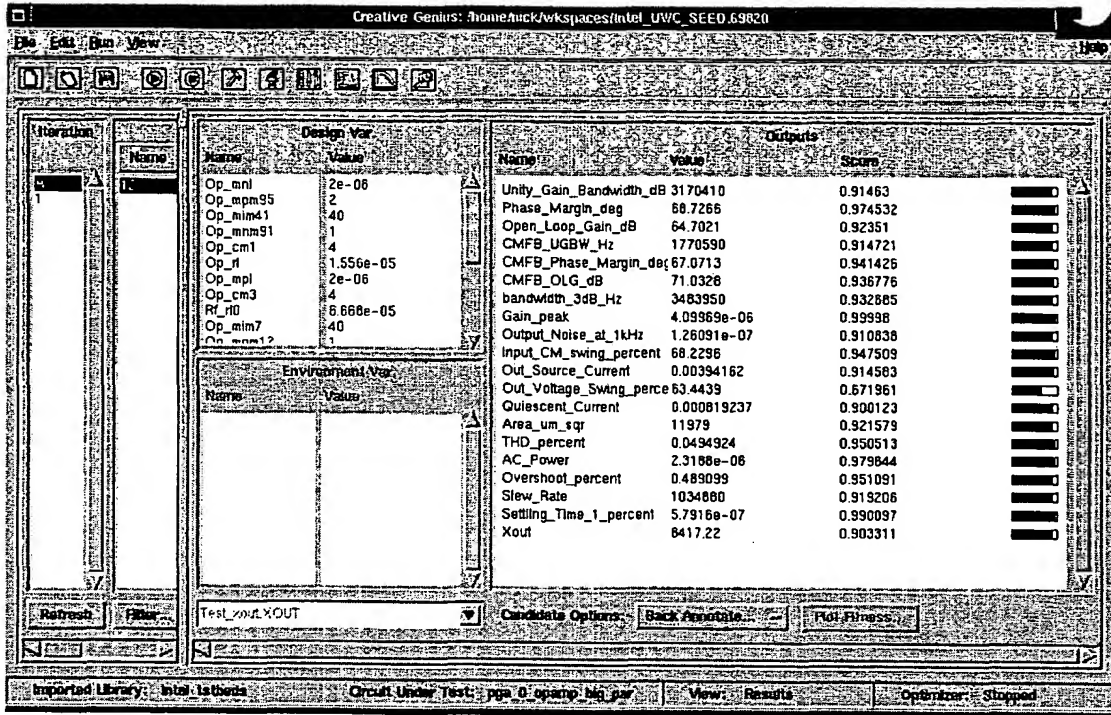


Figure 6

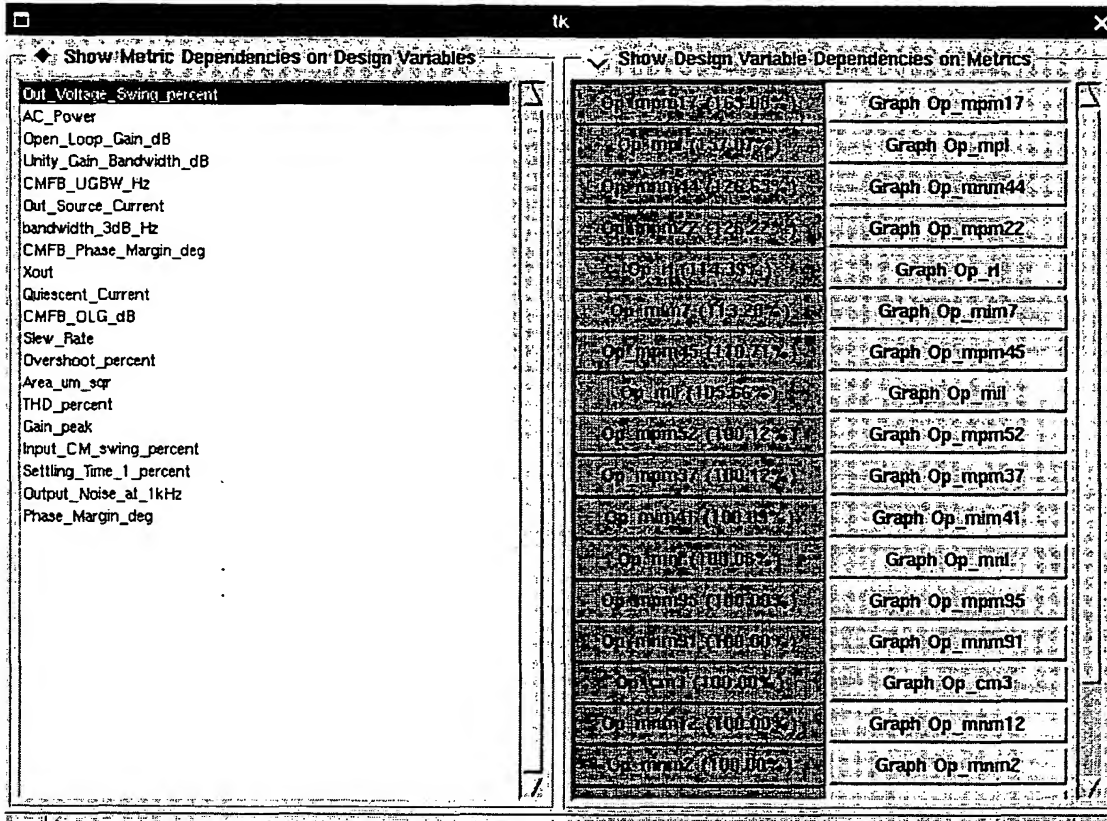


Figure 7

minimizing Out_Voltage_Swing_percent with Op_mnl causes the follow
Open Loop Gain dB (99.99%)
Unity Gain Bandwidth dB (102.43%)
CMFB Phase Margin deg (99.68%)
Quiescent Current (99.83%)
CMFB OLG dB (99.97%)
Supply Rejection (102.45%)
Over shoot percent (100.00%)
Recovery Time (100.23%)
THD percent (100.02%)
Gain Error (100.00%)

Figure 8

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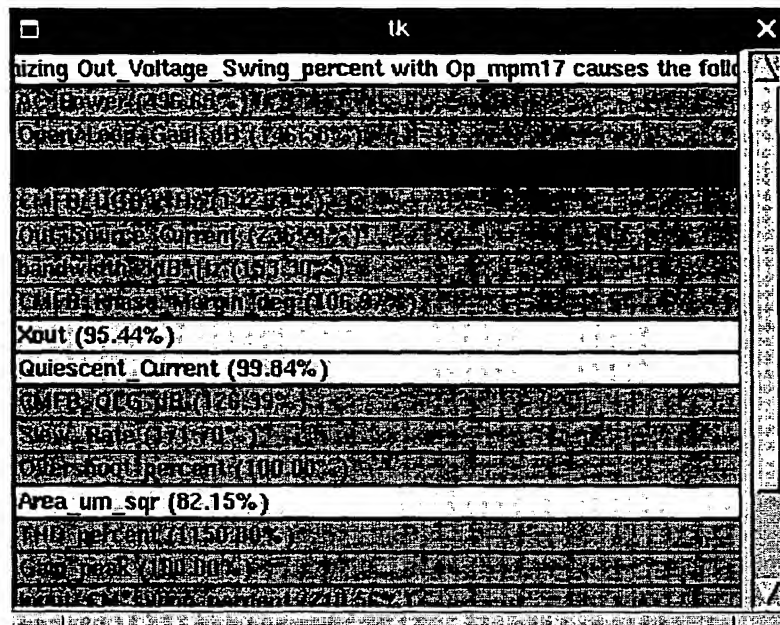


Figure 9

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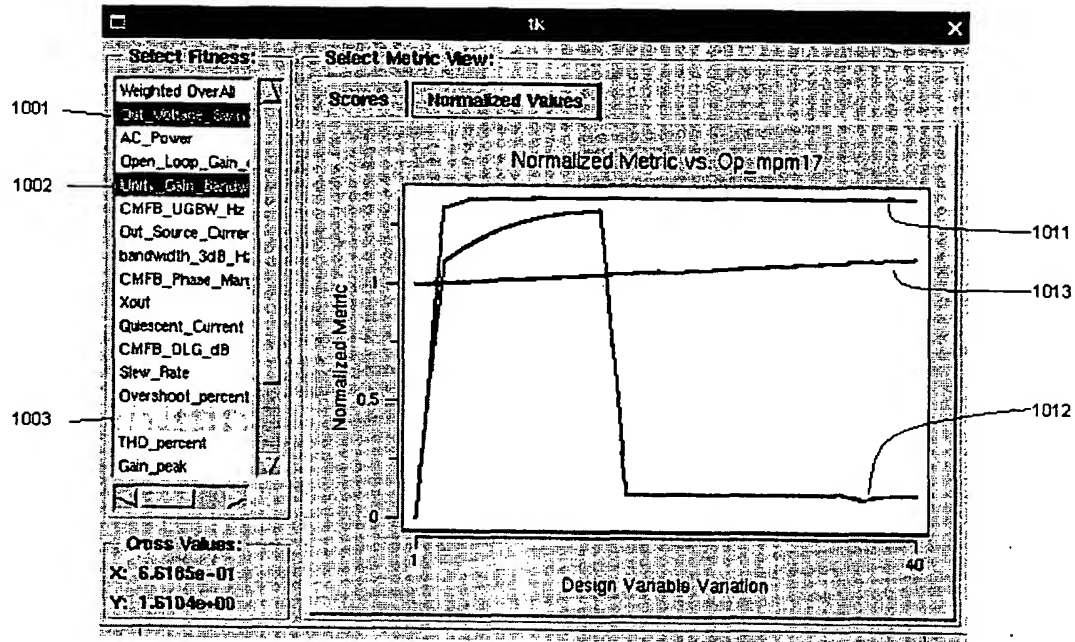


Figure 10

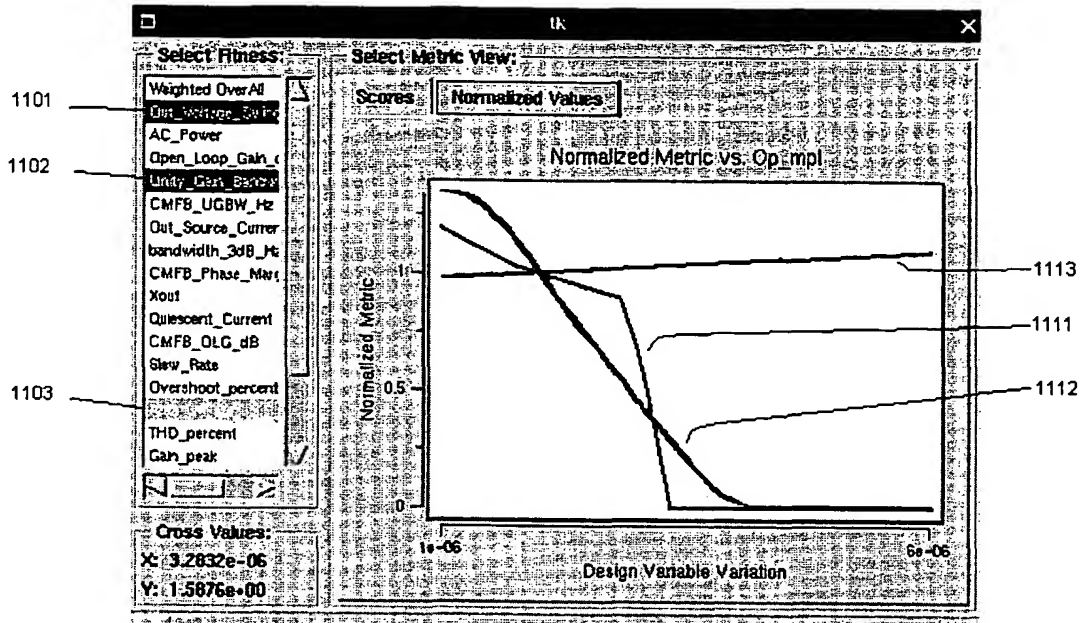


Figure 11

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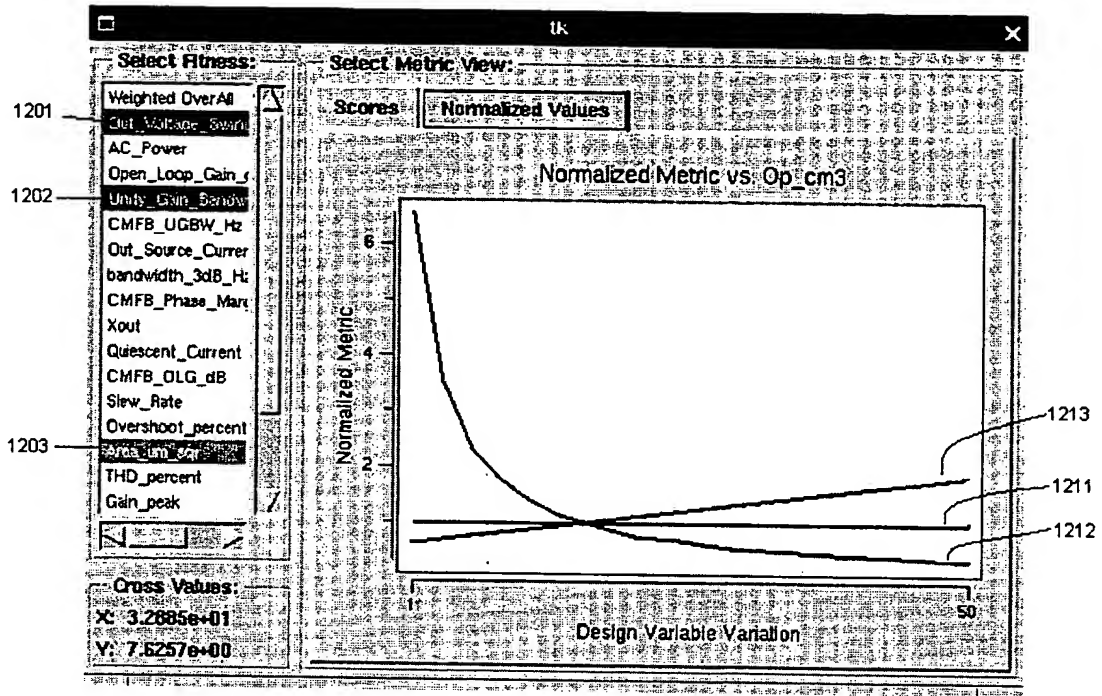


Figure 12

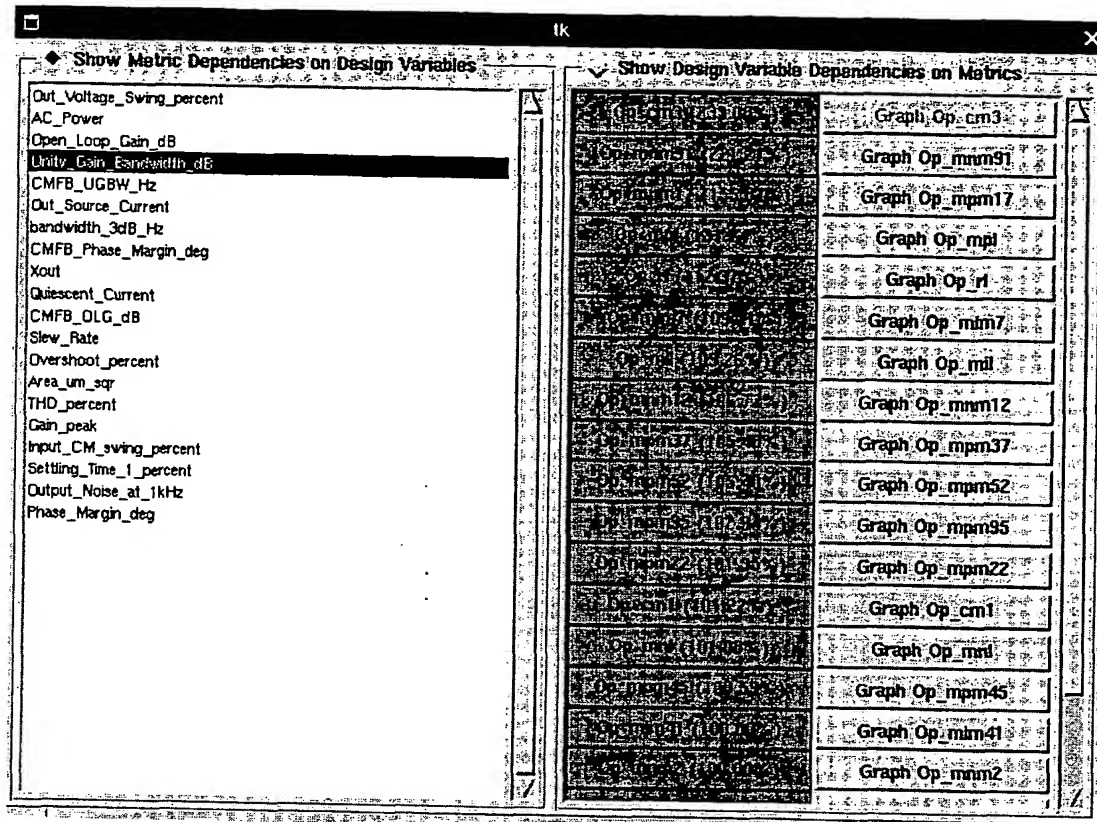


Figure 13

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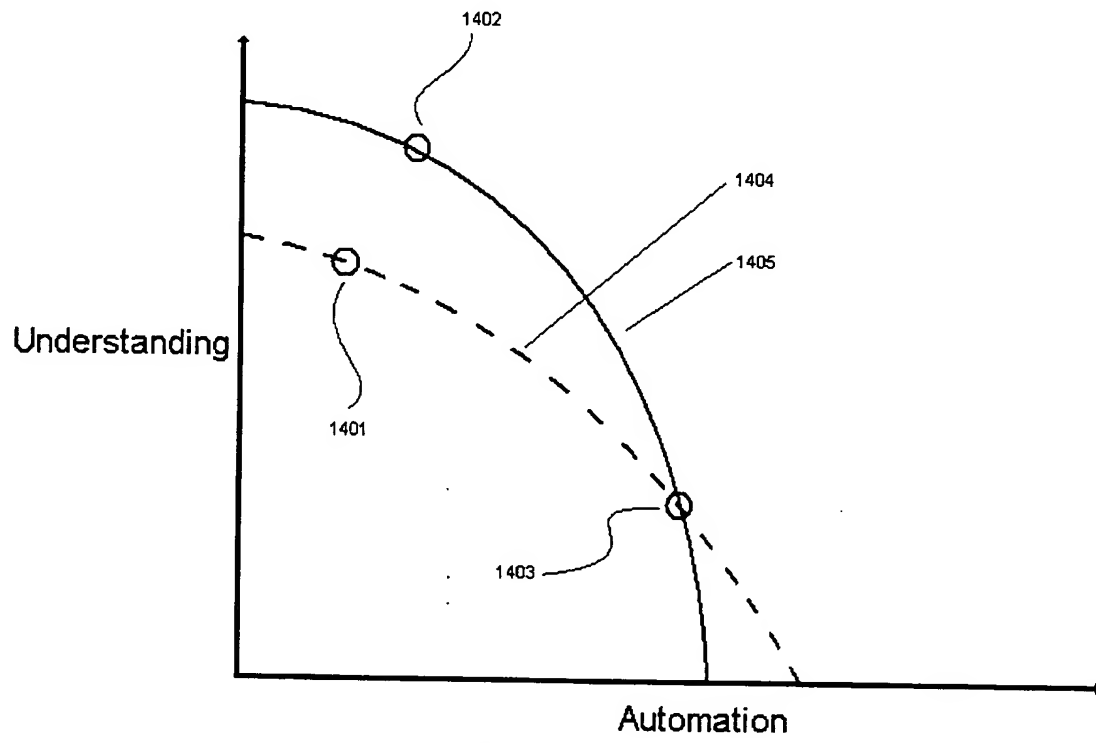


Figure 14

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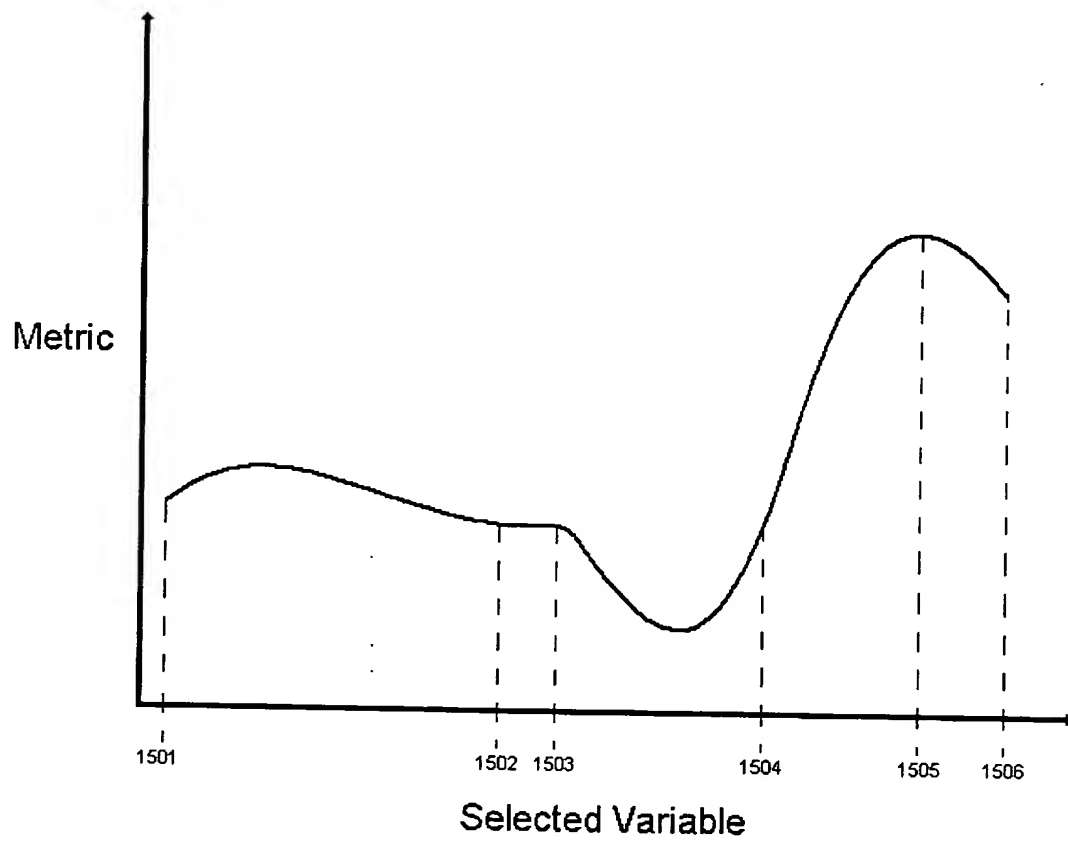


Figure 15